

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Hideo KUBOTA et al.

Appln. No.:

Filed: HERewith

For: SEMICONDUCTOR PROCESSING DEVICE

* * *

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any
assertion as to materiality or prior art effect, the
document listed on the attached Form PTO-1449 is hereby
cited.

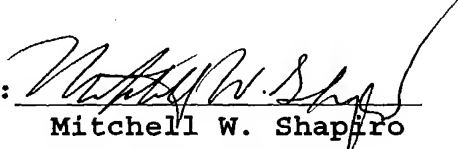
The document on the attached List was cited in the
specification, on pages 2 and 3, and its relevance is
indicated therein.

Respectfully submitted,

MWS:sjk

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By:


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July 18, 2003

FORM PTO-1449				Atty. Docket No. XA-9882		Appln. No.	
LIST OF DOCUMENTS CITED BY APPLICANT				Applicant Hideo KUBOTA et al.			
				Filing Date HEREWITH		Group	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
	AB						
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FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AJ	5-233844	09/10/93	Japan			Abstract
	AK						
	AL						
	AM						
	AN						
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OTHER (including author, title, date, pertinent pages, etc.)							
	AP						
	AQ						
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